



Attorney Docket No. 0553-0164.01

THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of:

Shunpei YAMAZAKI et al

Serial No: 10/651,458

Filed: August 29, 2003

Art Unit:

For: SEMICONDUCTOR DEVICE AND METHOD  
OF FABRICATING THE SAME

) I hereby certify that this correspondence  
) is being deposited with the United States  
) Postal Service as first class mail in an  
) envelope addressed to:  
) Commissioner for Patents, P.O. Box 1450,  
) Alexandria, VA 22313-1450, on September  
) 17, 2003

Cristie M. Nell

Date: September 17, 2003

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

Pursuant to 37 C.F.R. §1.97, as revised on February 4, 1992, 1135 OG 23-24, Applicant hereby calls the Examiner's attention to documents listed on the attached form, which documents may be material to the examination of this application. A copy of each of the references was submitted to the Patent Office or was cited by the Examiner in the parent application, Serial No. 09/517,542, filed March 2, 2000, which is being relied upon for an earlier filing date under 35 U.S.C. 120 (37 C.F.R. §1.98(d)).

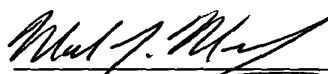
No inference should be drawn that the attached list sets forth a comprehensive investigation of the prior art, that any or all are pertinent to the invention, or that any apparatus disclosed is equivalent to the subject invention.

The citation of the above-discussed documents is not to be construed as an assertion that more pertinent art could not possibly be in existence. Citation of any document herein is not to be construed as an admission that any subject matter disclosed in the document is necessarily within the inventive field of endeavor, that any disclosure is necessarily prior in time to a particular date which may be relevant to the instant patent application, and/or that any disclosure is otherwise necessarily prior art with respect to the instant invention.

Applicant also respectfully reserves the right to later set forth how the instant invention is distinguished over the disclosure of any document or other art, including the disclosure of those documents discussed herein, that may be cited by the Examiner in rejecting a claim in the instant patent application.

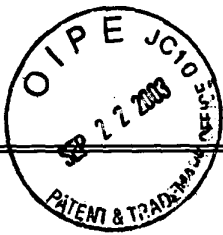
A first office action, notice of allowance or issue fee notification has not been received in this case, so applicant does not believe that a fee is due. However, if any such fee is required, please charge our Deposit Account No. 50/1039.

Respectfully submitted,



Mark J. Murphy  
Registration No: 34,225

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LIST OF PUBLICATIONS  
CITED BY APPLICANT

Atty. Docket No.  
0553-0164.01

Serial No.  
10/651,458

Applicant  
Shunpei YAMAZAKI  
et al

Filing Date  
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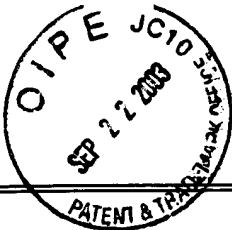
Group

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB- CLASS	FILING DATE
	5,463,483	10/1995	Yamazaki	359	58	07/20/94
	5,594,569	01/14/97	Konuma et al	349	122	
	5,856,689	01/1999	Suzawa	257	296	
	5,904,514	05/1999	Konuma et al	438	165	
	5,998,841	12/1999	Suzawa	257	350	
	6,162,654	12/2000	Kawabe	438	30	02/26/99
	6,208,392	03/27/01	Miller et al	349	84	
	6,271,543	08/07/01	Ohtani et al	257	72	

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	NAME	English Abstract	English Trans.	FILING DATE



OTHER PRIOR ART - NON-PATENT LITERATURE DOCUMENTS  
(Including Author, Title, Date, Pertinent Pages)

- 1) INUI, S. et al, "Thresholdless Antiferroelectricity in Liquid Crystals and its Application to Displays," J. Matter Chem., vol. 6, no. 4, pp. 671-673, (1996).
- 2) YOSHIDA, T. et al, "A Full-Color Thresholdless Antiferroelectric LCD Exhibiting Wide Viewing Angle with Fast Response Time," SID 97 Digest, pp. 841-844, (1997).
- 3) SATO, F. et al, "High Resolution and Bright LCD Projector with Reflective LCD Panels," SID 97 Digest, vol. 28, pp. 997-1000, May 13-15, 1997.
- 4) FURUE, H. et al, "Characteristics and Driving Scheme of Polymer-Stabilized Monostable FLCs Exhibiting Fast Response Time and High Contrast Ratio with Gray-Scale Capability," SID 98 Digest, pp. 782-785, (1998).
- 5) KUROGANE, H. et al, "Reflective AMLCD for Projection Displays: D-ILA," SID 98 Digest, vol. 29, pp. 33-36, May 17-22, 1998.
- 6) NAGATA, T. et al, "Silicon-Chip-Based Reflective PDLC Light Valve for Projection Display," SID 98 Digest, vol. 29, pp. 37-39, May 17-22, 1998.
- 7) DOVE, D.B., "High Performance Projection Displays Based on Reflective LC Silicon Light Valves," IDW '98, pp. 741-744, December 7-9, 1998.
- 8) HIROTA, S. et al, "A Silicon-Chip-Based Light Valve with Reflective Twisted Nematic Mode for High-Definition Projectors," IDW '99, pp. 985-988, December 1-3, 1999.
- 9) US Patent Application No. 09/252,813 (issue fee) to Ohtani et al, filed February 19, 1999, including specification, claims, abstract, drawings and PTO filing receipt.
- 10) US Patent Application No. 09/498,646 (pending) to Yamazaki et al, filed February 7, 2000, including specification, claims, abstract, drawings and PTO filing receipt.
- 11) US Patent Application No. 09/671,654 (pending) to Yamazaki et al, filed September 28, 2000, including specification, claims, abstract, drawings and PTO filing receipt.

EXAMINER:

DATE CONSIDERED:

\*EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP form. Draw line through citation if not in conformance and not considered. Include a copy of this form with the next communication to applicant.